

PG166-A02

TU116,192b GDDR6 2CHx16,90W/120W
DVI-D DL + HDMI + DP

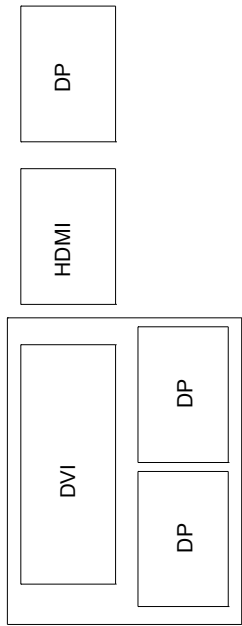
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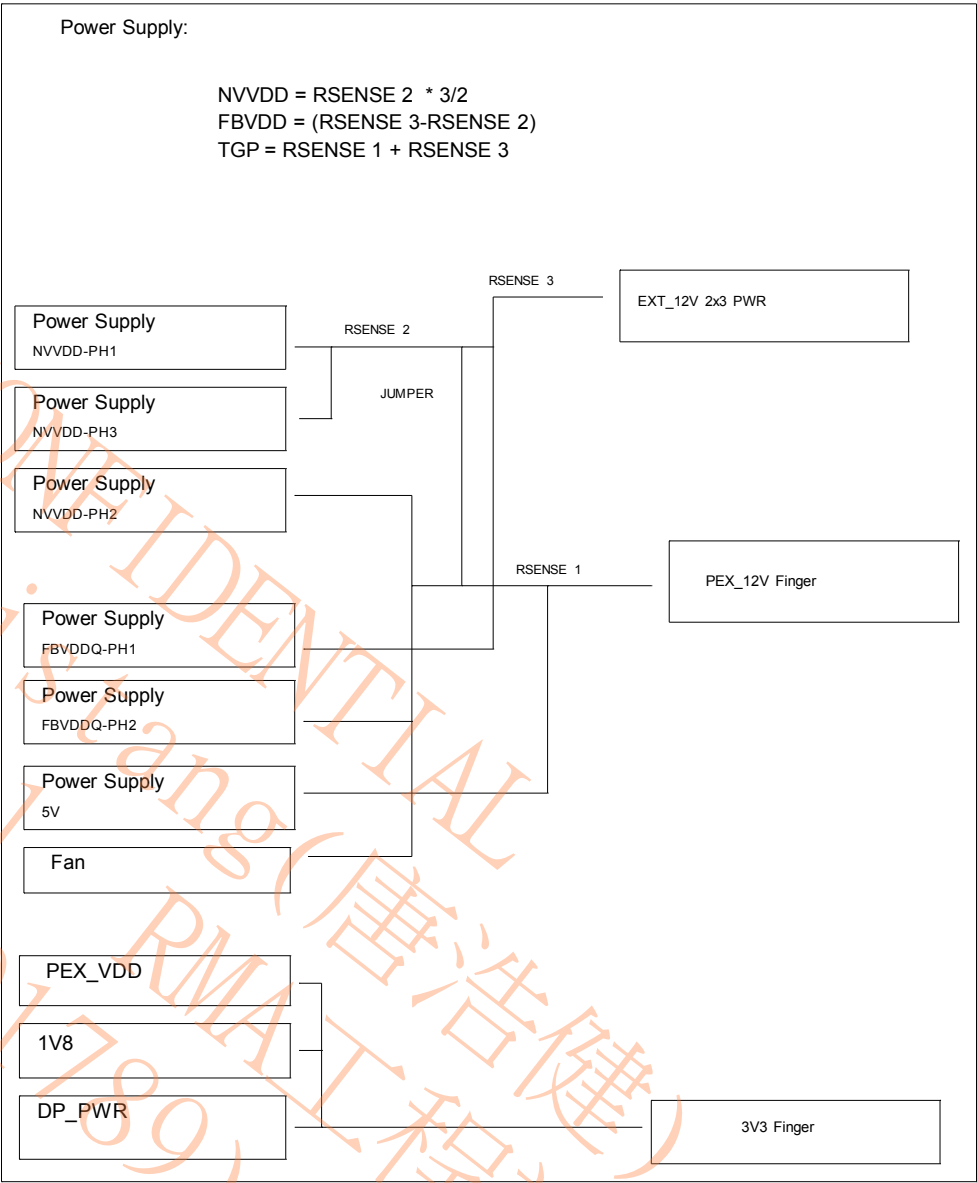
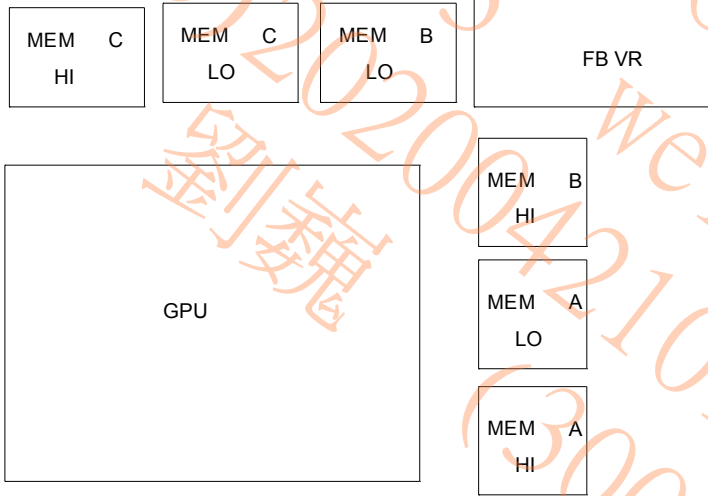
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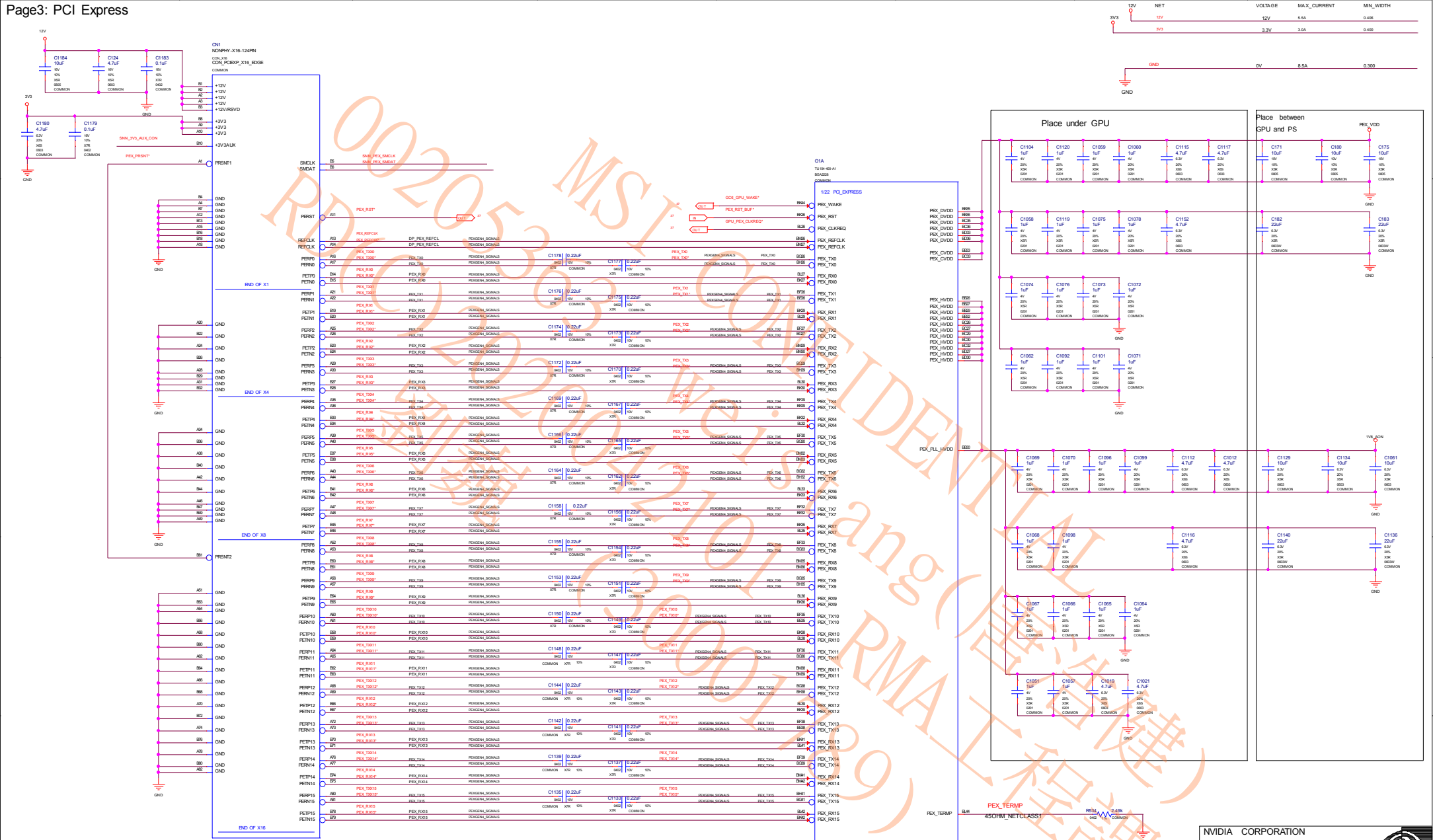


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ASSEMBLY	ASSEMBLY DESCRIPTION
PCI Express	PCI Express

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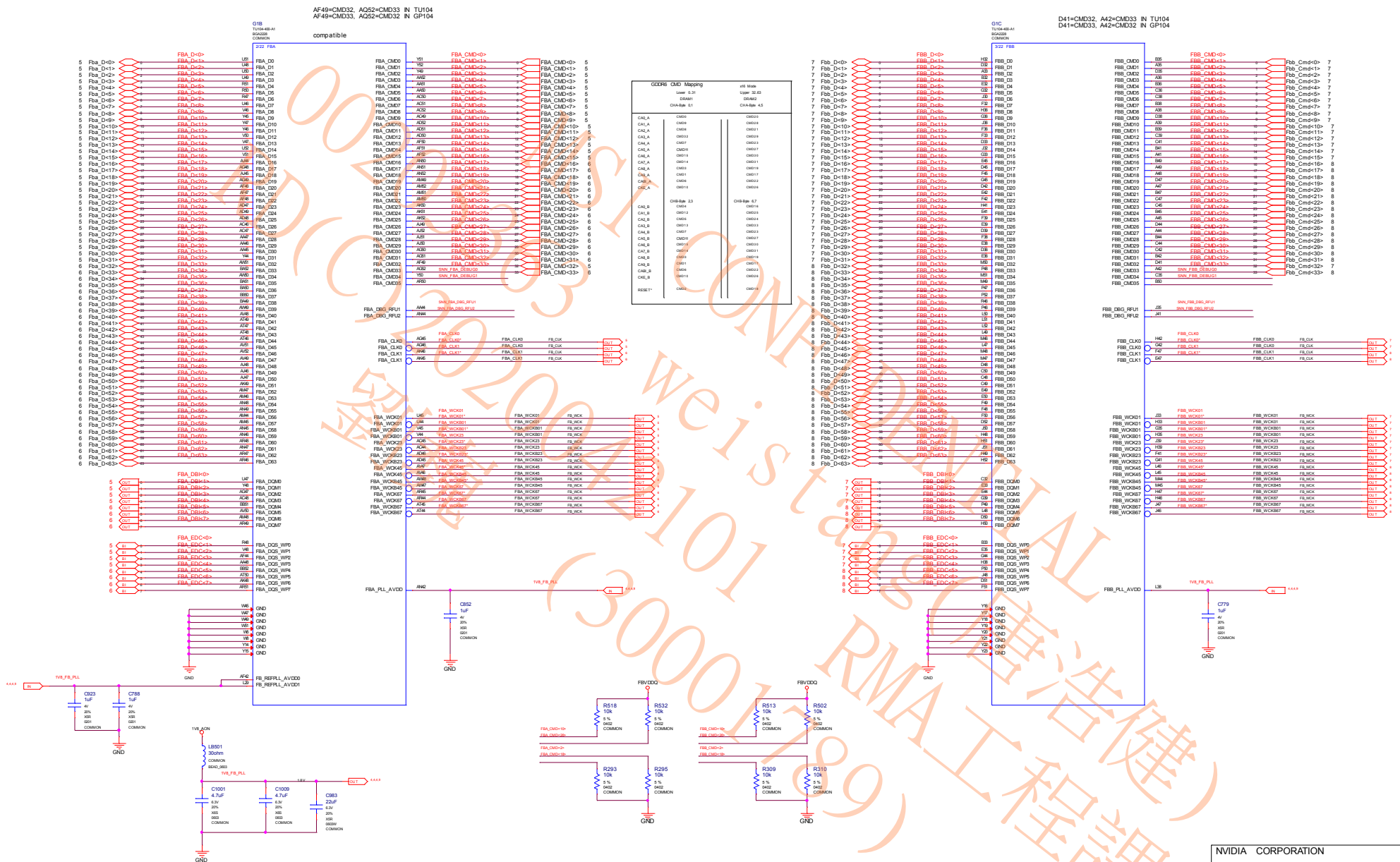
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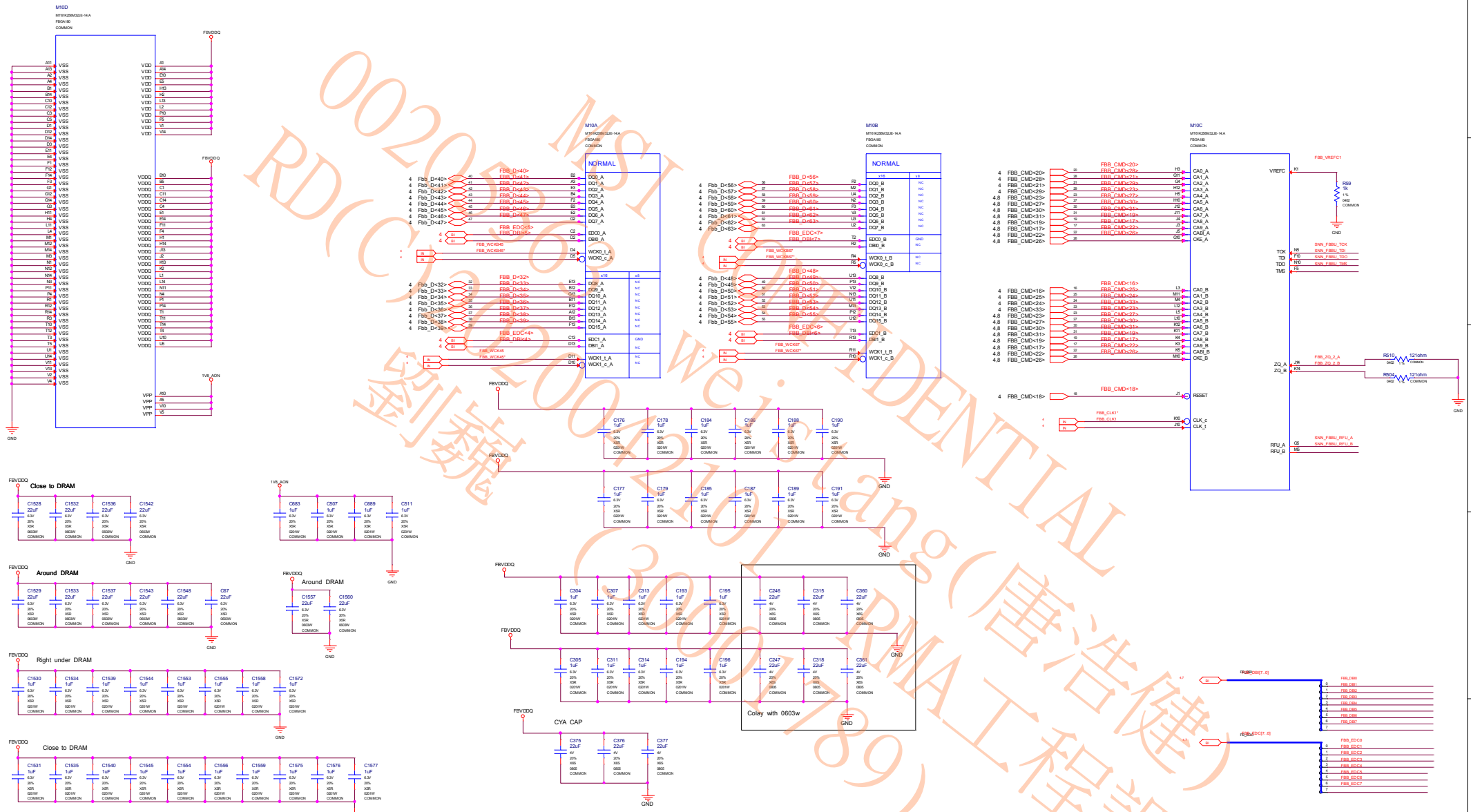
10166-A02

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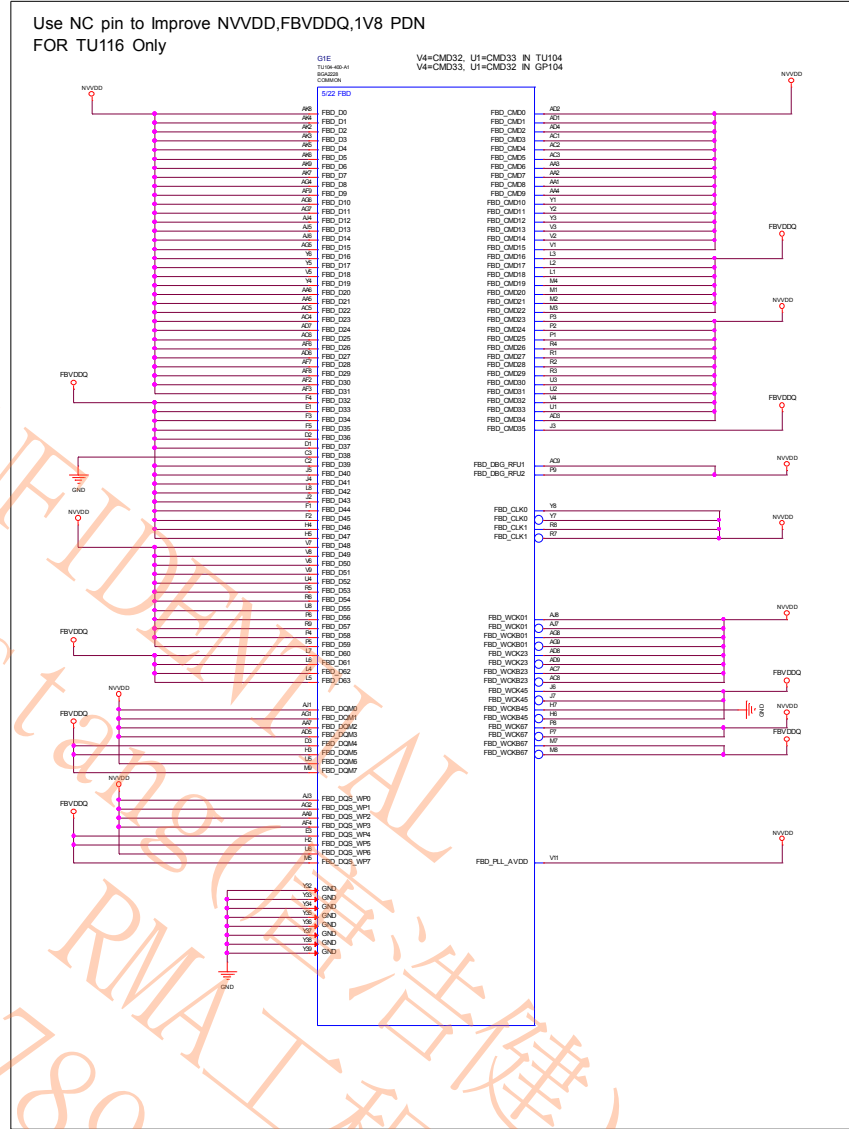
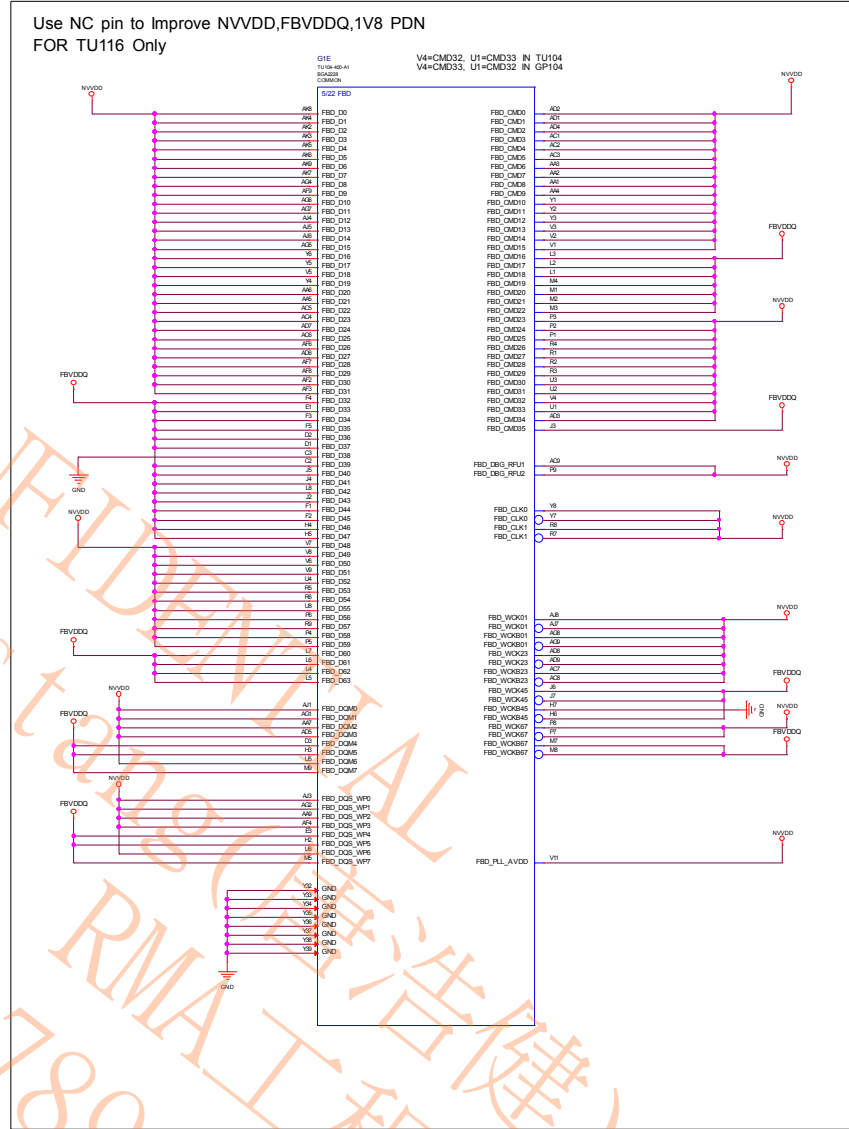
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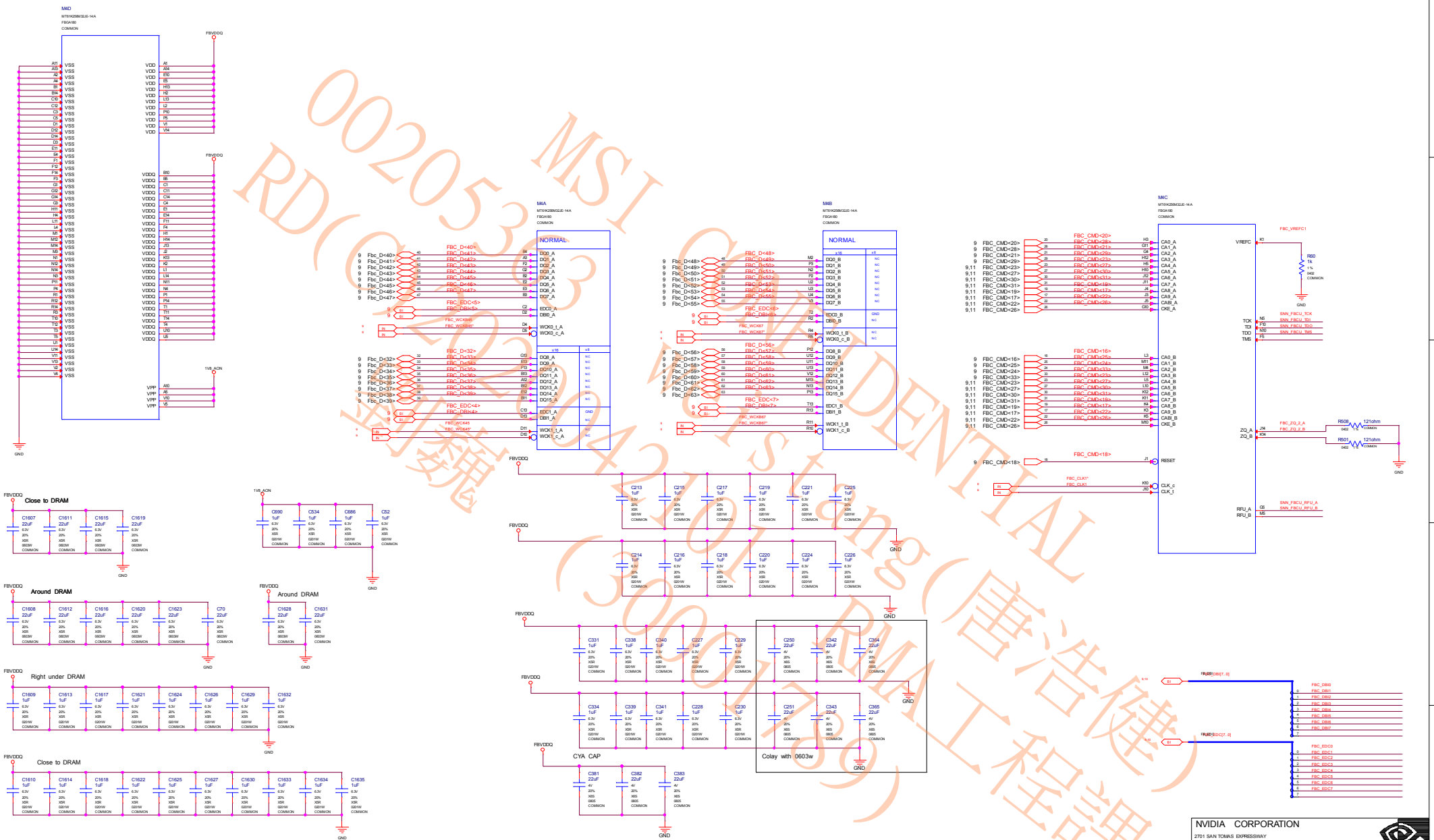




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ASSEMBLY DESCRIPTION
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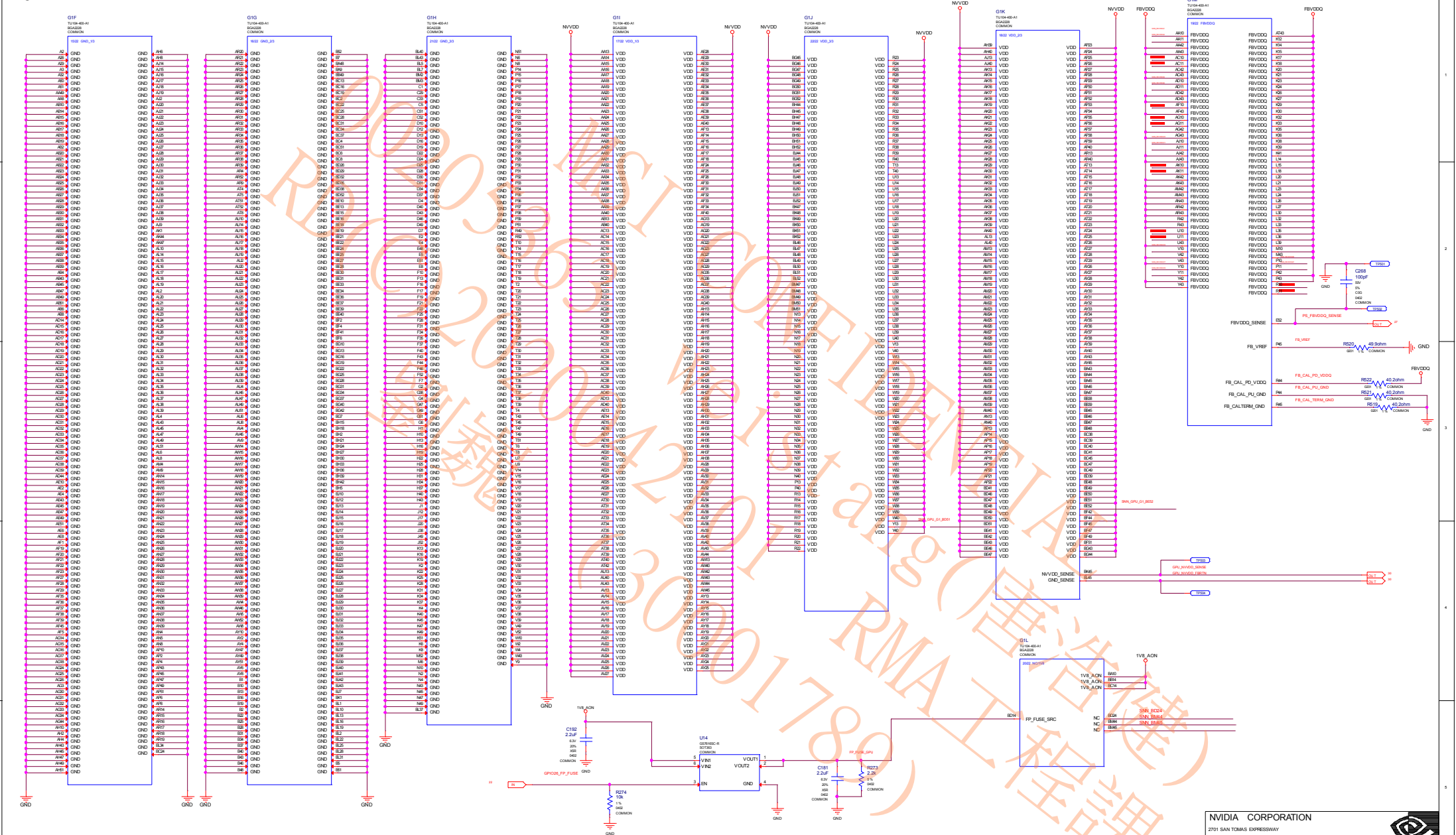
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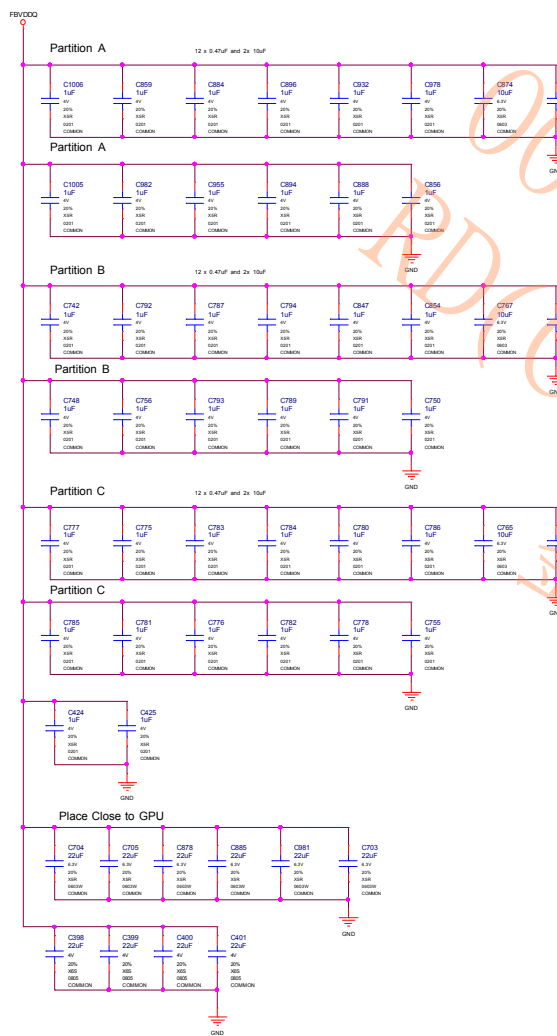
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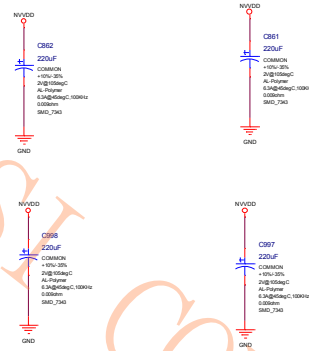


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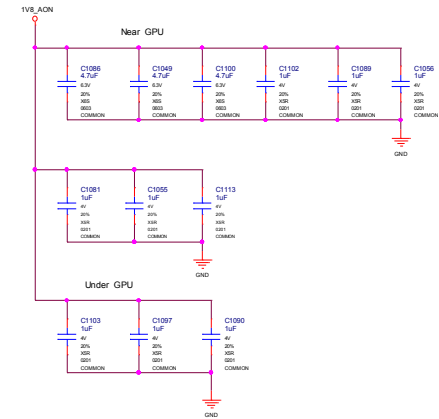
FBVDDQ



NVDD



1V8_AON

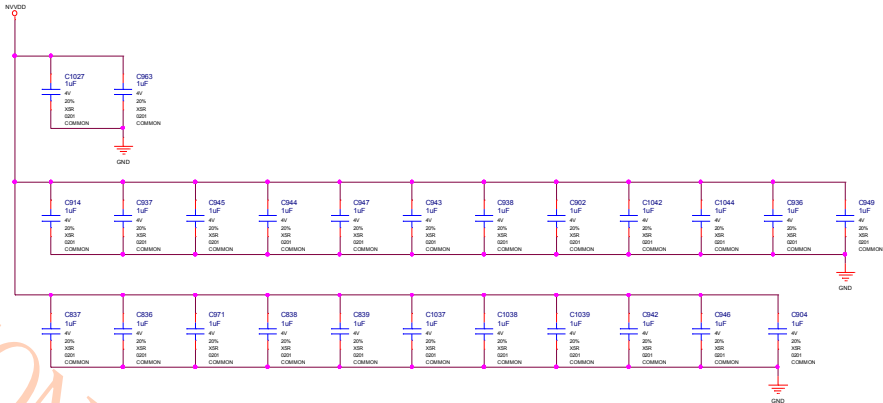
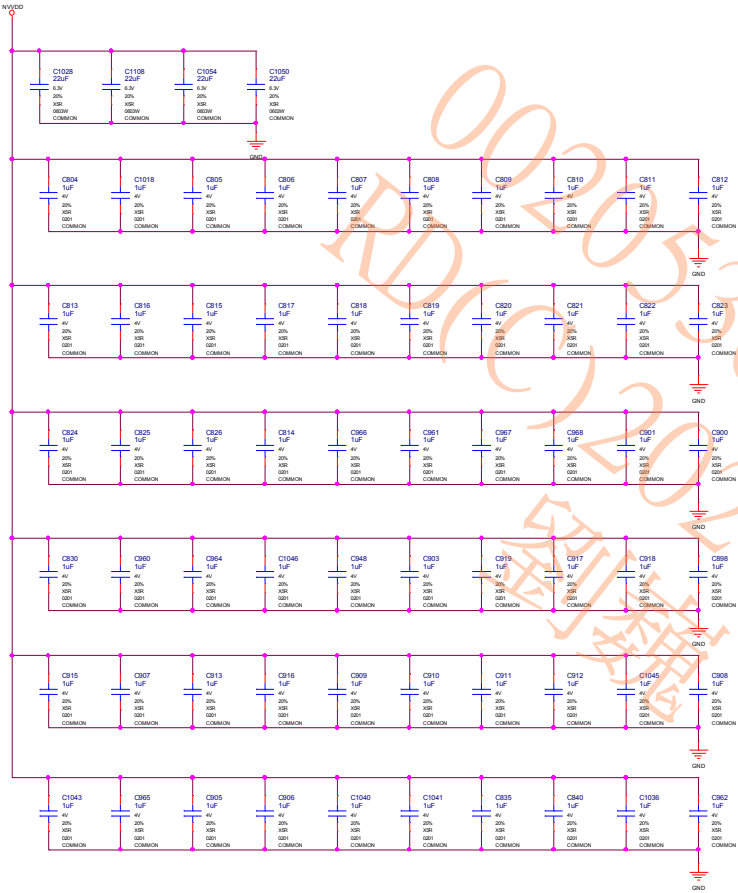


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ASSEMBLY	ASSEMBLY DESCRIPTION
FRGE DETAIL	GPU DECOUPLING 1

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NVDD

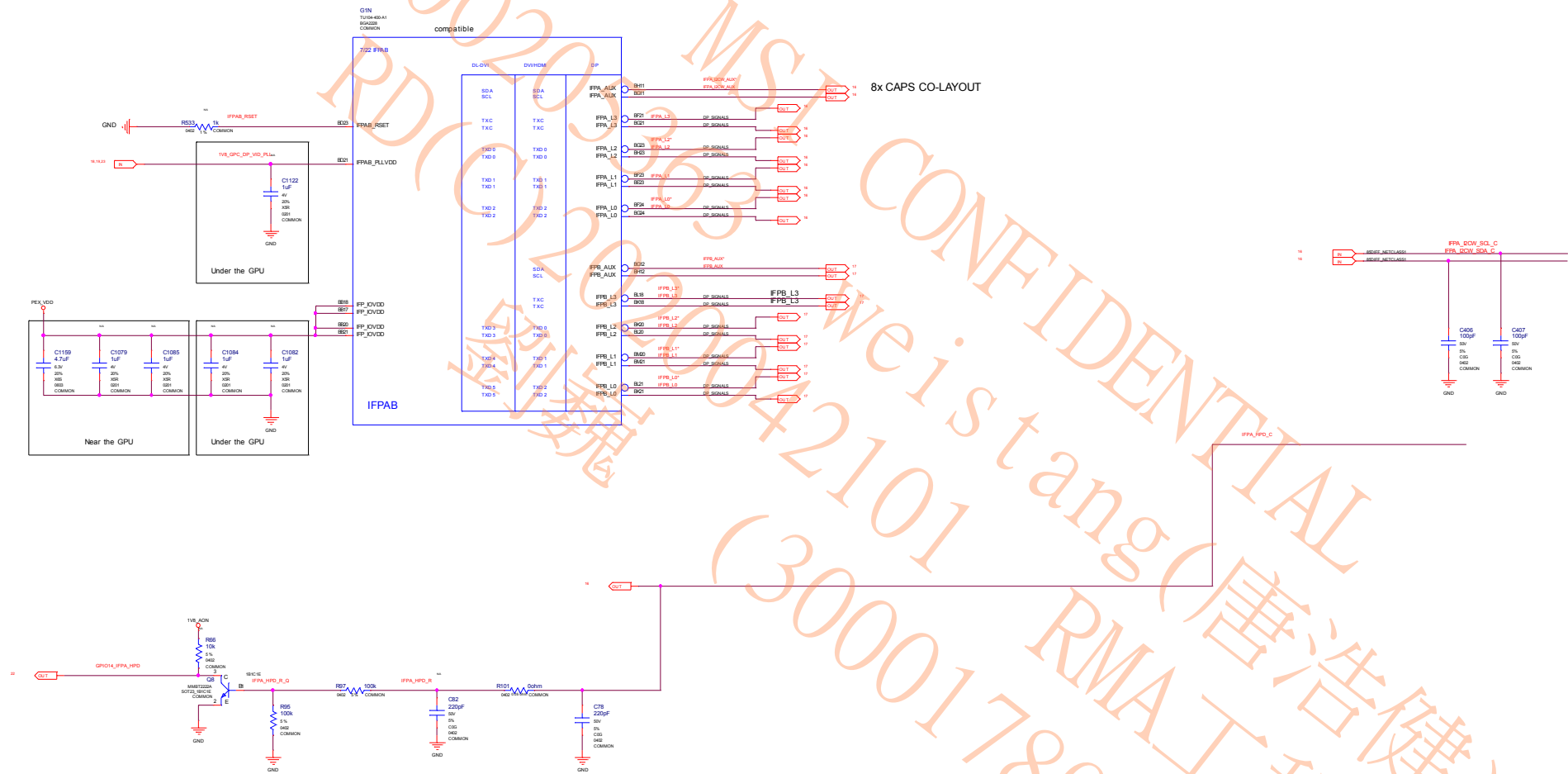


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ASSEMBLY	ASSEMBLY DESCRIPTION*
PCB DETAIL	GPU DECOUPLING 2

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NV_PN	600-1G166-BASE-A02		
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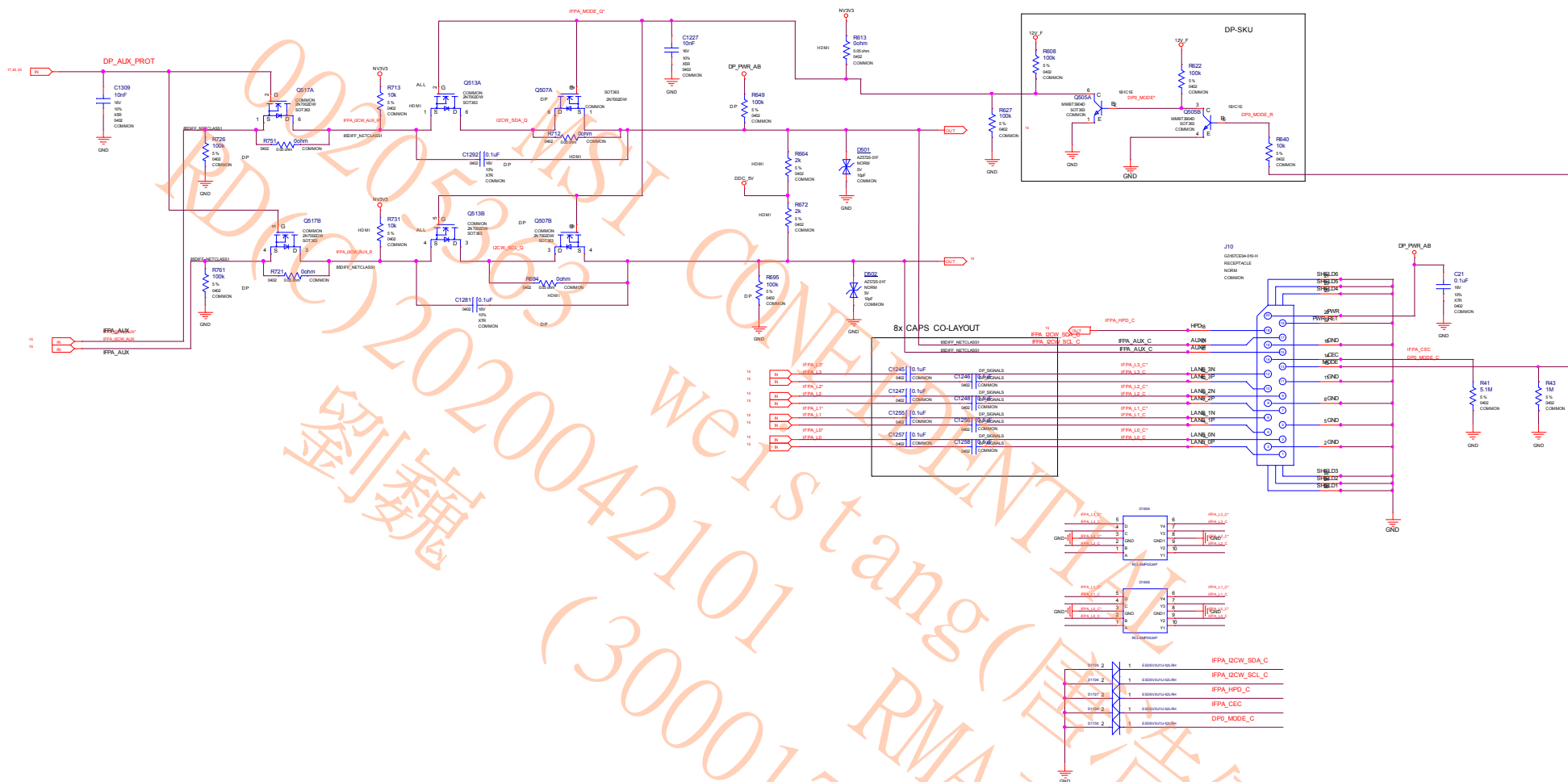


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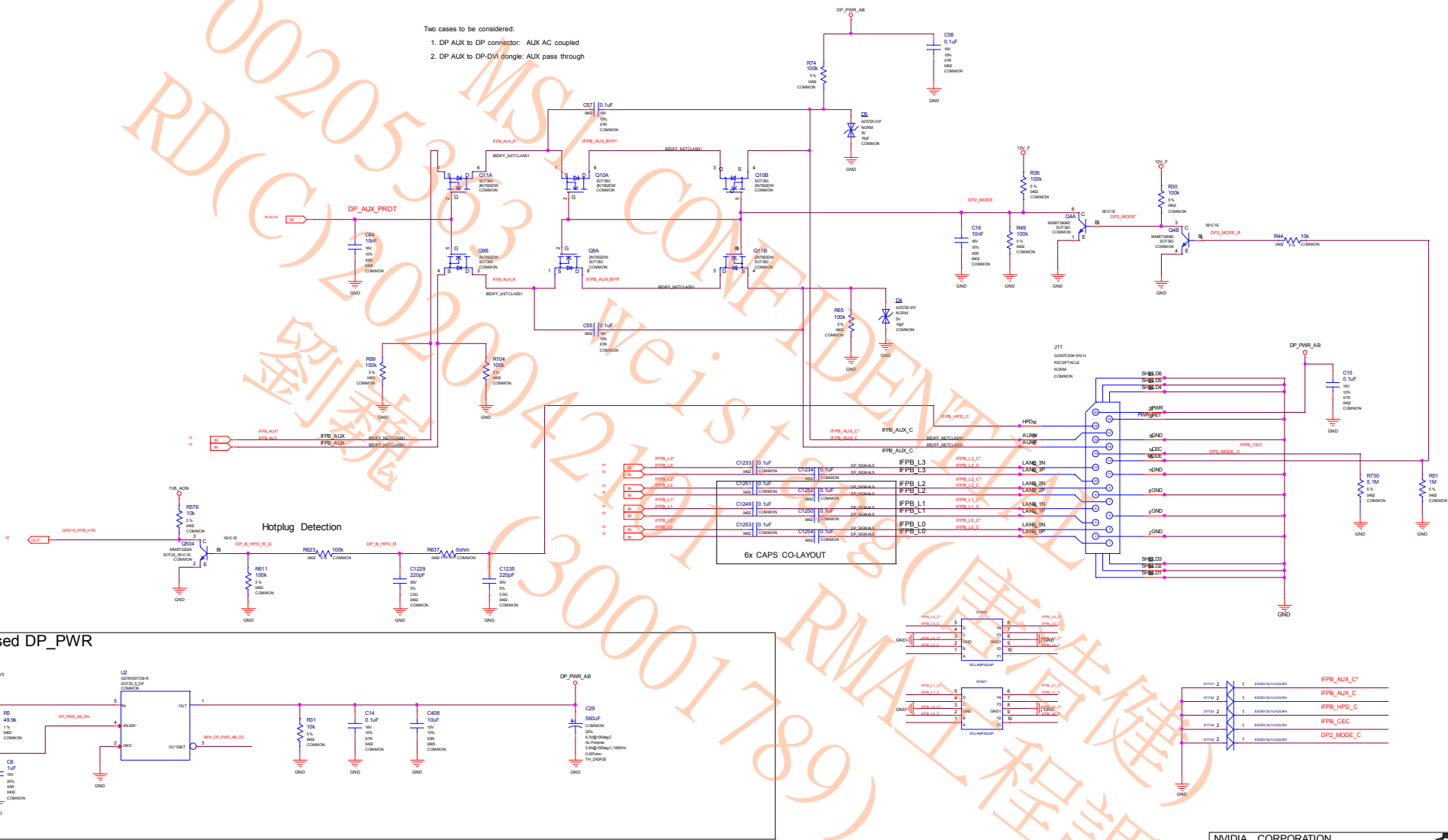




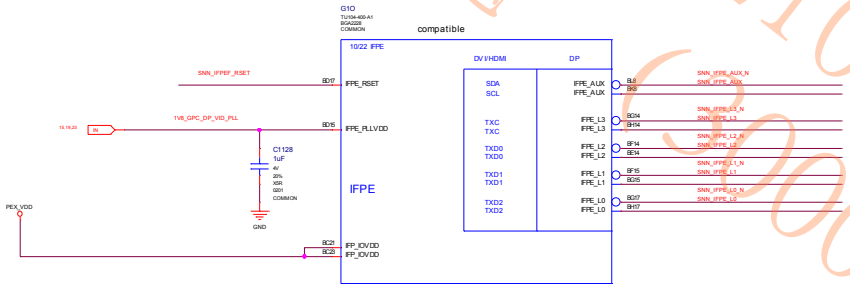
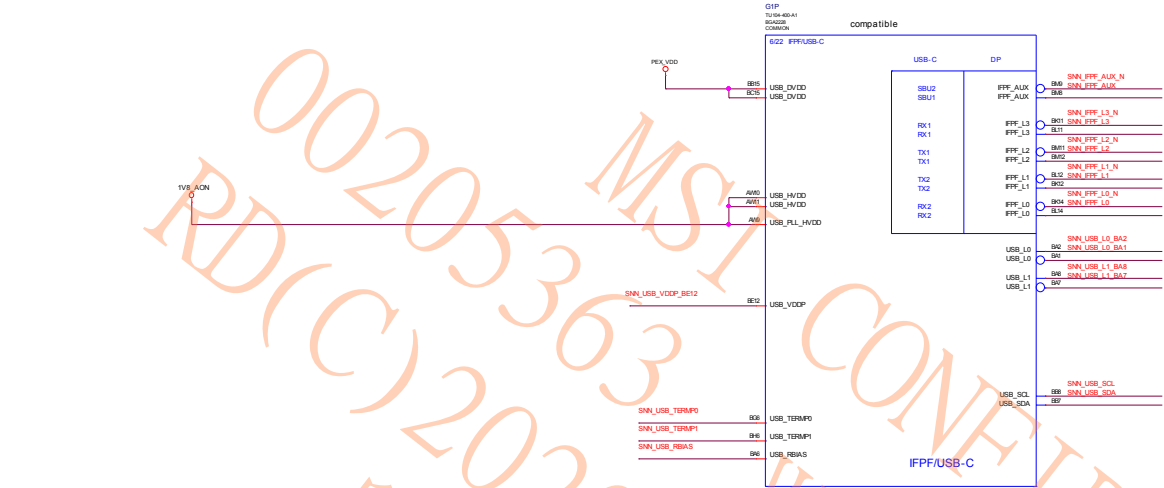
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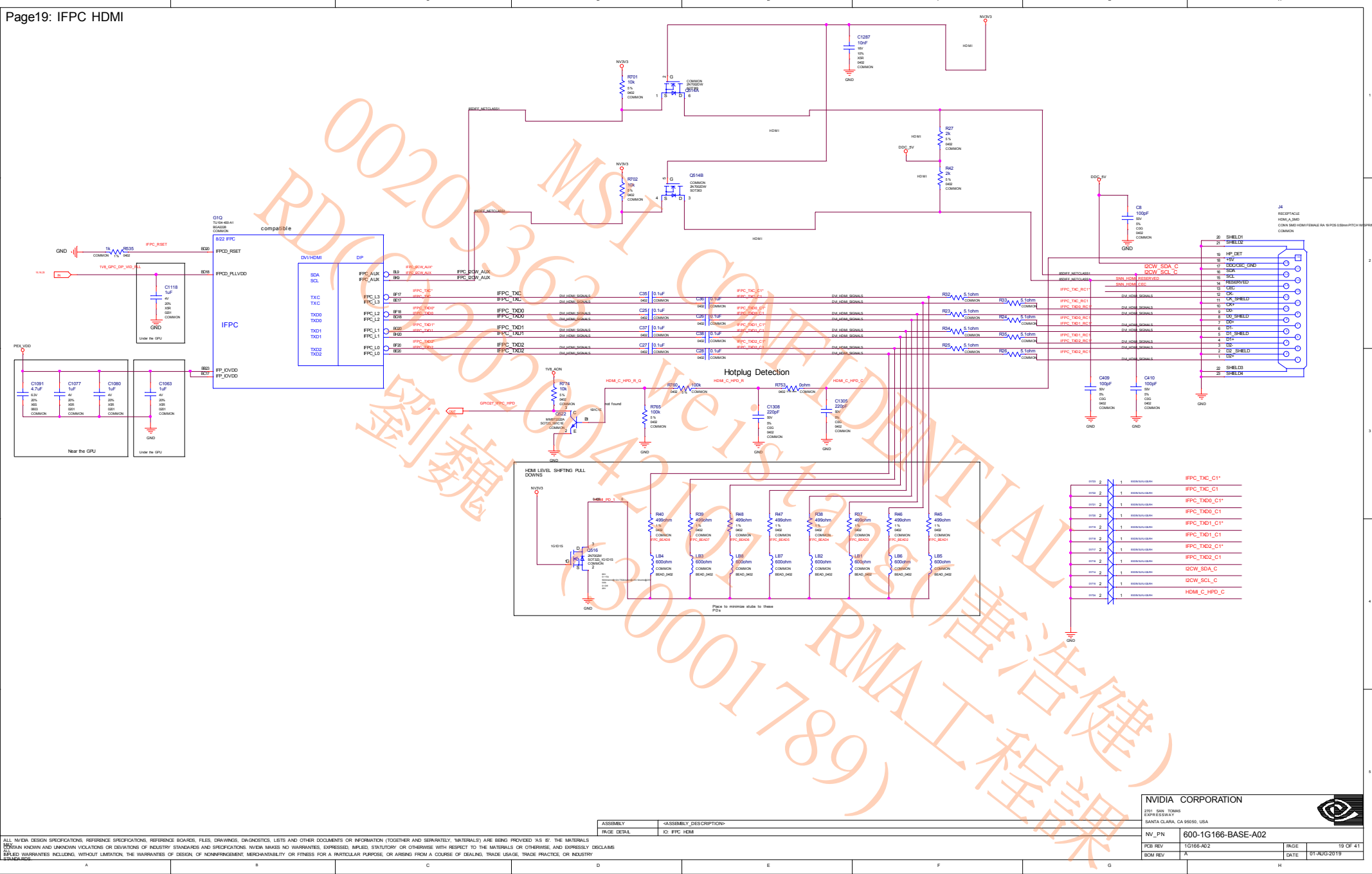
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ASSEMBLY	ASSEMBLY DESCRIPTION
IFPE & IFPF NC	

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ASSEMBLY	ASSEMBLY DESCRIPTION
IFPC HDMI	IFPC HDMI

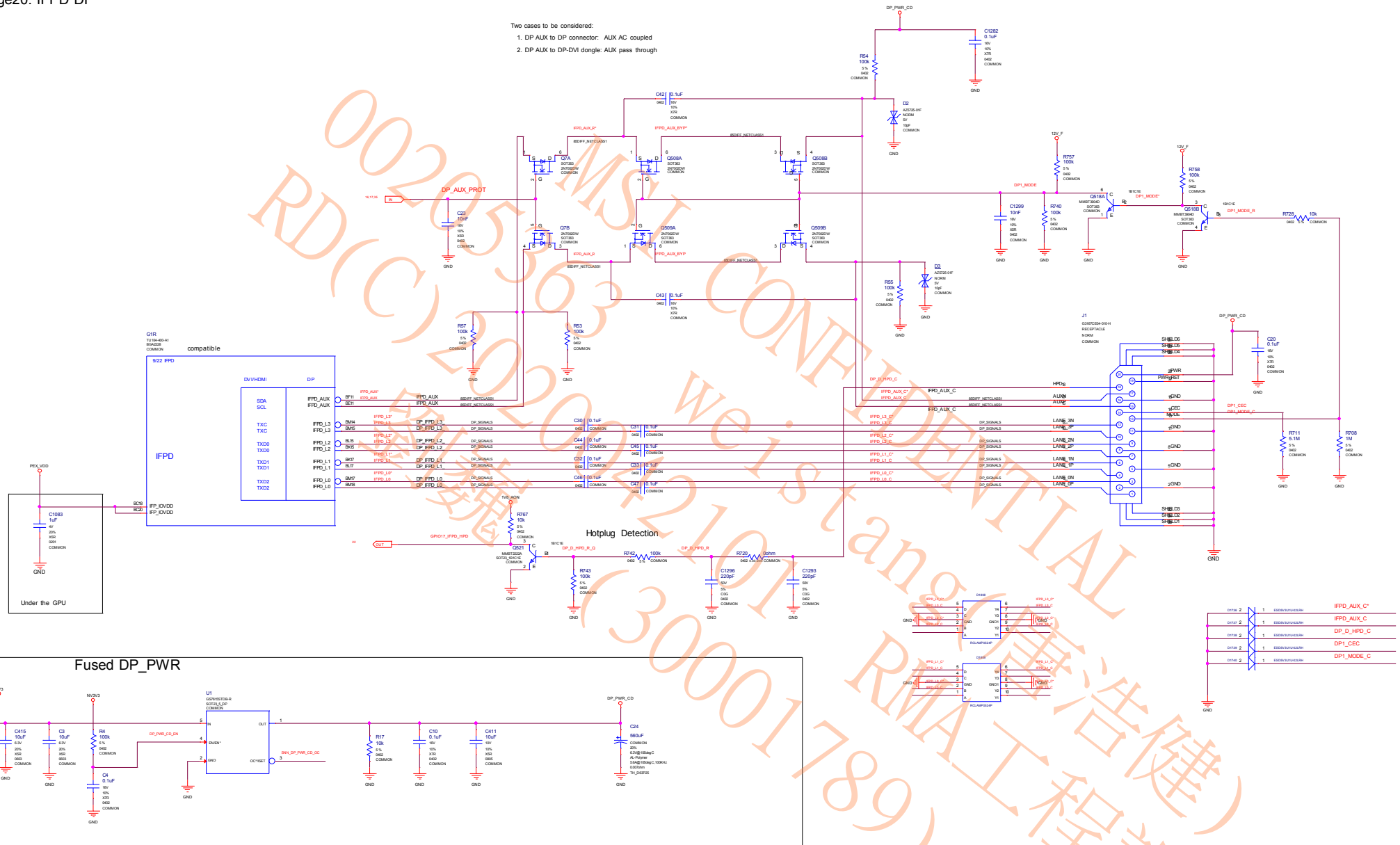
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Two cases to be considered:

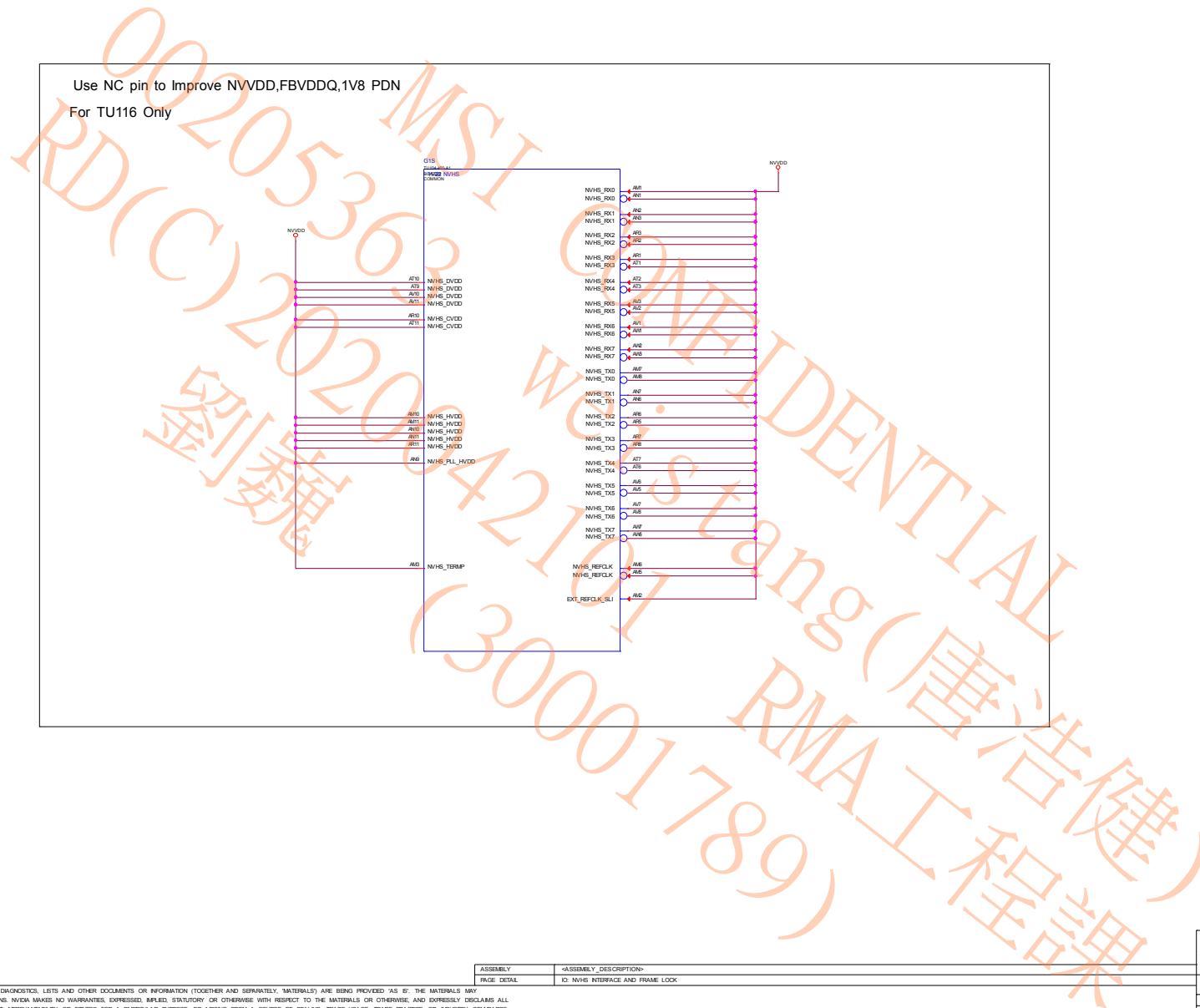
1. DP AUX to DP connector: AUX AC coupled
2. DP AUX to DP-DVI dongle: AUX pass through



ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	JO: IFFD DP

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A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T	U	V	W	X	Y	Z
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STRAP2	STRAP1	STRAP0	RAMCFG[4:0]
L	L	L	00000
L	L	H	00001
L	H	L	00010
L	H	H	00011
H	H	L	00110
H	H	H	00111
L	L	M	01000

STRAP2	STRAP1	STRAP0	RAMCFG[4:0]	
L	L	L	0000	Samsung 8Gb 256Mx32 K4Z80325BC-HC14
L	L	H	0001	Micron 8Gb 256Mx32 MT61K256M32JE-14-A
L	H	L	0010	Hynix 8Gb 256Mx32 H56C8H24MJR-S2C
L	H	H	0011	Samsung 8Gb 256Mx32 K4Z80325BC-HC12
H	H	L	0110	
H	H	H	0111	
L	L	M	1000	
L	M	L	1001	

ROM_SO	ROM_SI	ROM_SCLK	DUMMY[2:0].FS_OVERT	1.ENABLE 0.DISABLE	
L	L	L	XXX1	FS_OVERT ENABLE	DEFAULT
L	L	M	XXX0	FS_OVERT DISABLE	

STRAP5	STRAP4	STRAP3	SMB_ALT_ADDR	DEVID_SEL	PCI_CFG	VGA_DEVICE
M	H	H	1	1	1	1
M	H	L	1	1	1	0
M	L	H	1	1	0	1
M	L	L	1	1	0	0
L	H	M	1	0	1	1
L	M	H	1	0	1	0
L	M	L	1	0	0	1
L	L	M	1	0	0	0
H	H	H	0	1	1	1
H	H	L	0	1	1	0
H	L	H	0	1	0	1
H	L	L	0	1	0	0
L	H	H	0	0	1	1
L	H	L	0	0	1	0
L	L	H	0	0	0	1 DEFAULT
L	L	L	0	0	0	0

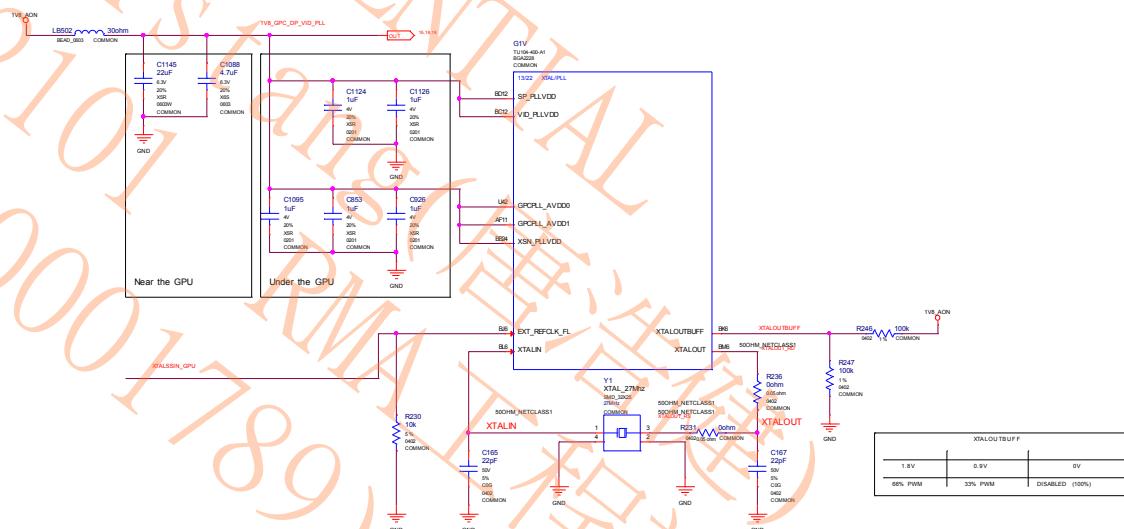
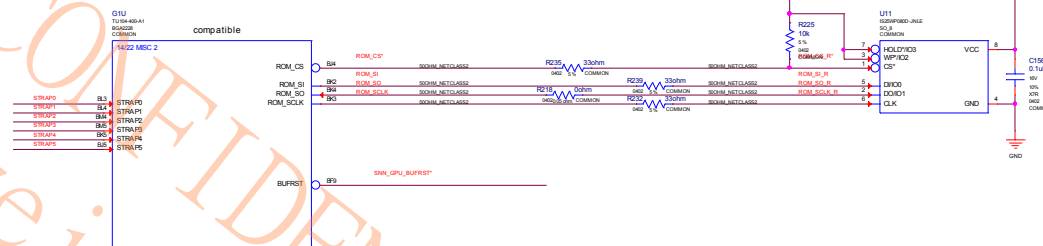
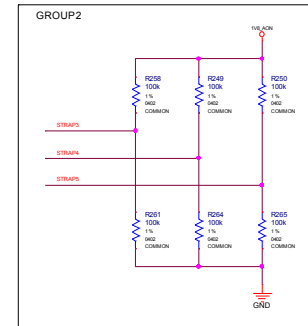
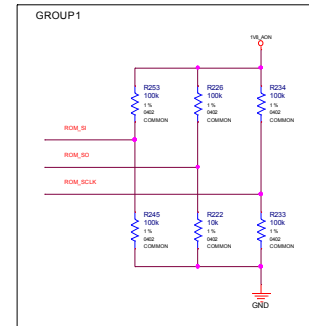
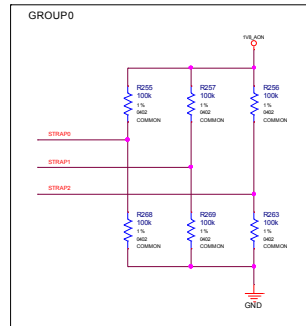
H=High :Tied to 1.8V
M=Middle:Tied to 0.9V
L=Low :Tied to 0V

1:SMB_ALT_ADDR ENABLE
0:SMB_ALT_ADDR DISABLE

1:DEVID_SEL REBRAND
0:DEVID_SEL ORIGINAL

1:PCI_CFG LOW POWER
0:PCI_CFG HIGH POWER

1:VGA_DEVICE ENABLE
0:VGA_DEVICE DISABLE



XTALOUTBUFF		
1.8V	0.9V	0V
80% PWM	30% PWM	DISABLED (100%)

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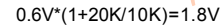
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1v8 Switcher

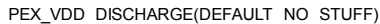



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PEX_VDD Switcher

3V3 F is Far Away From Source

OVR0



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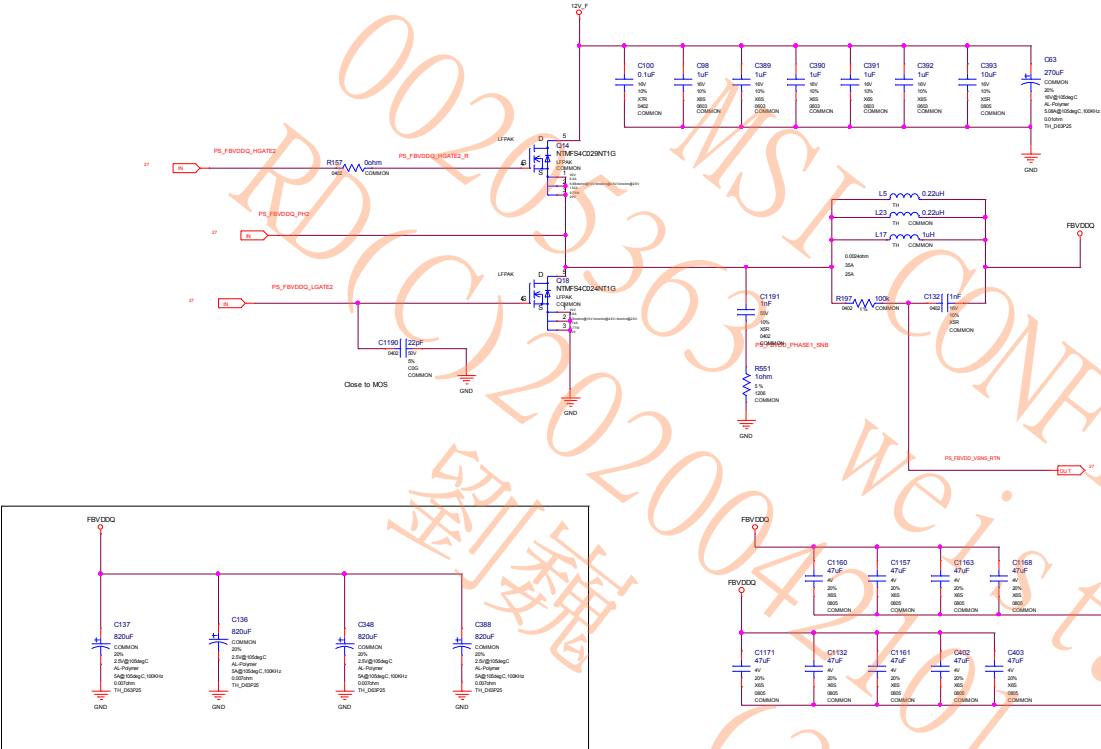
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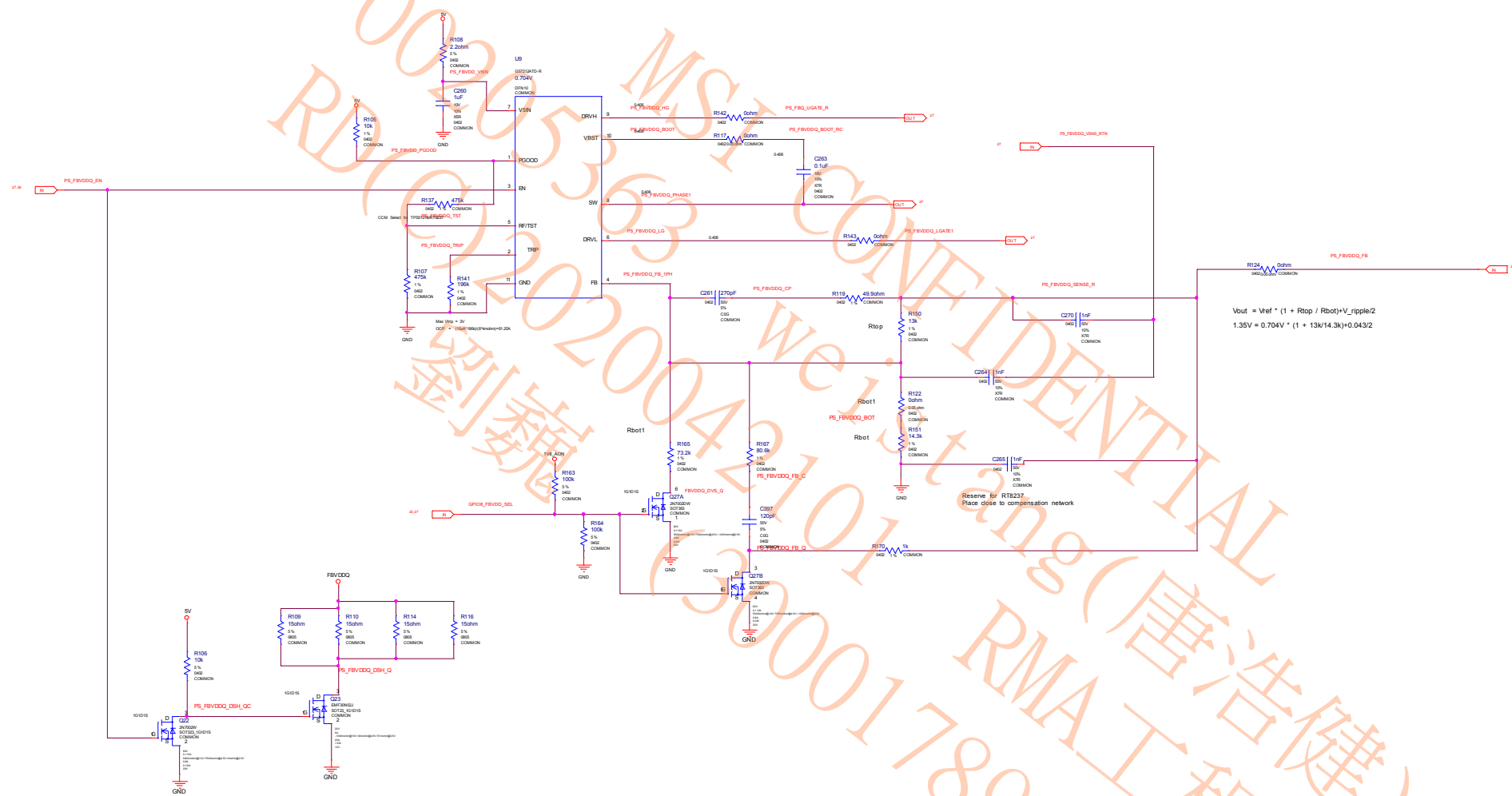


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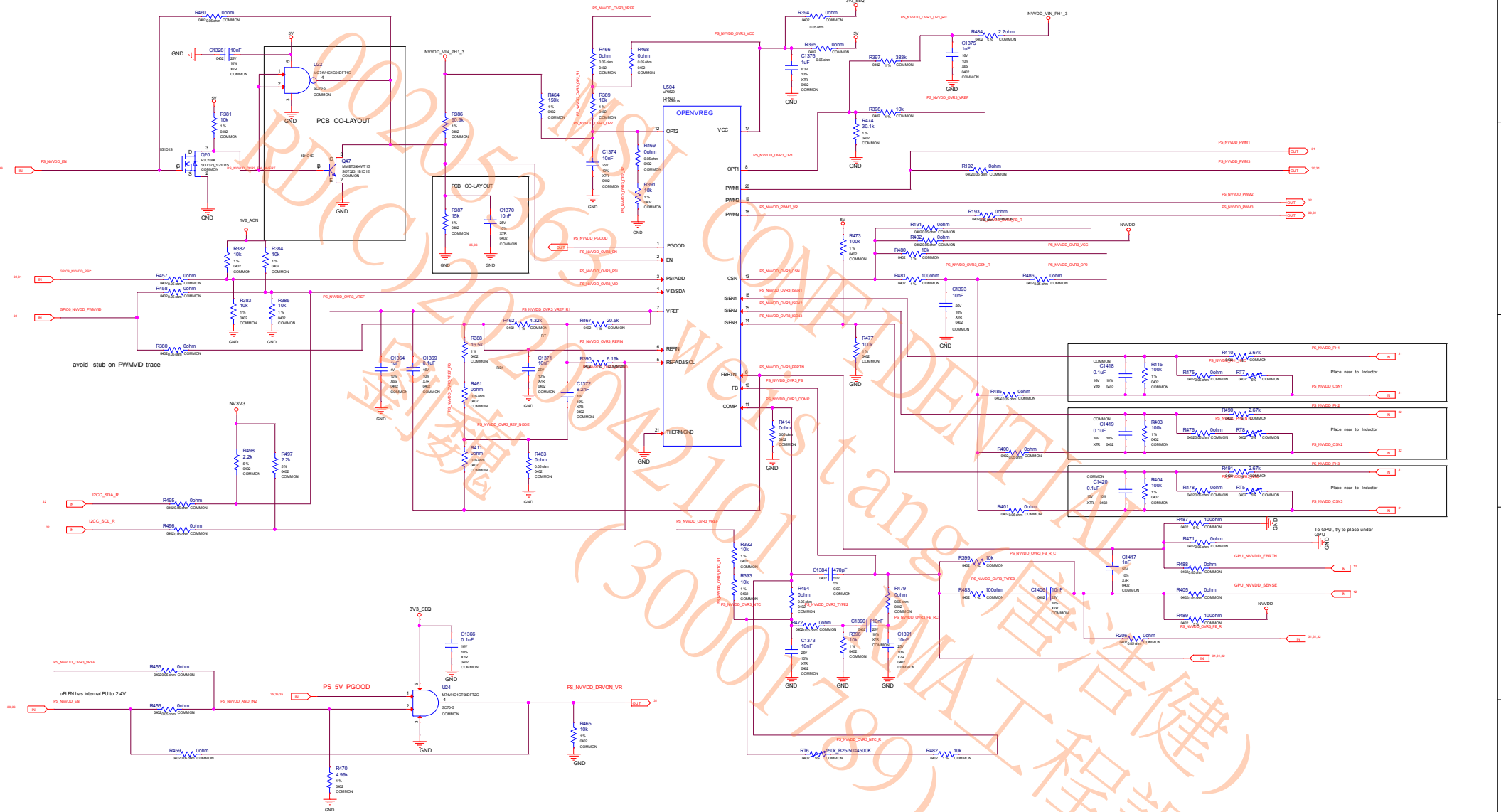



$$V_{out} = V_{ref} * (1 + R_{top} / R_{bot}) + V_{ripple}/2$$
$$1.35V = 0.704V * (1 + 13k/14.3k) + 0.043/2$$

89) RMA (唐浩健) 工程學

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ASSEMBLY	ASSEMBLY DESCRIPTION
PS: NVVDD CTR	PS: NVVDD Controller

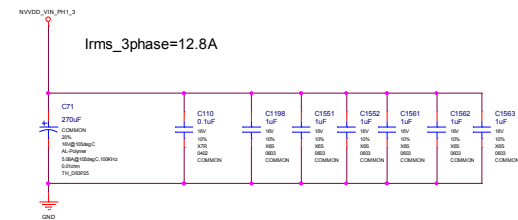
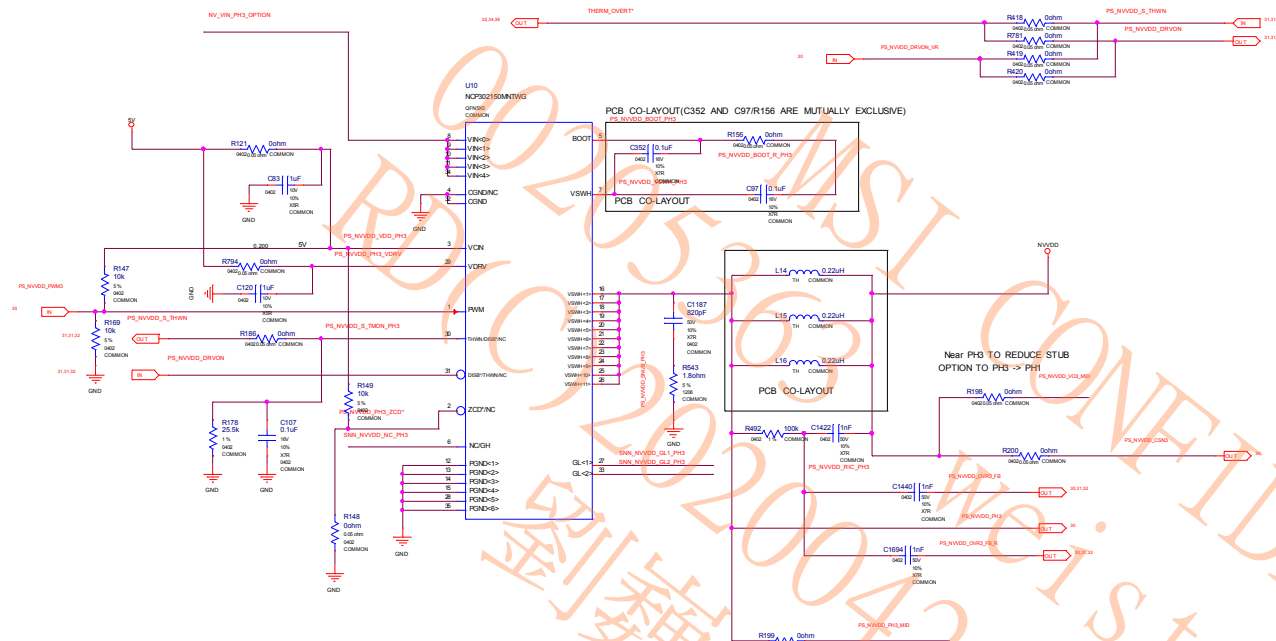
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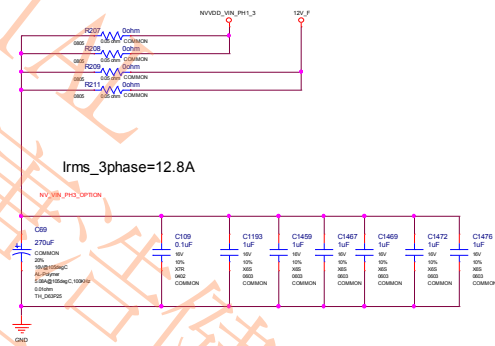
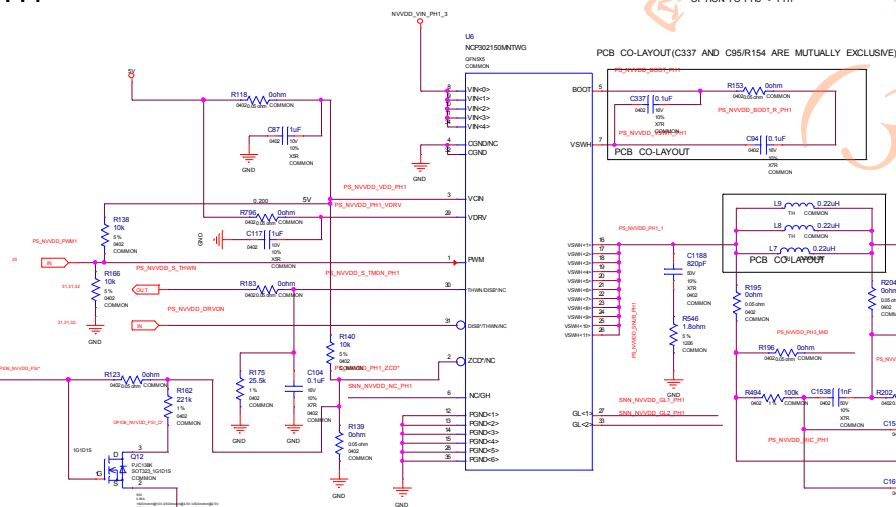
NV_PN 600-1G166-BASE-A02

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PH3



PH1



COMMON	
ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PS: NVDD Phase 1.3

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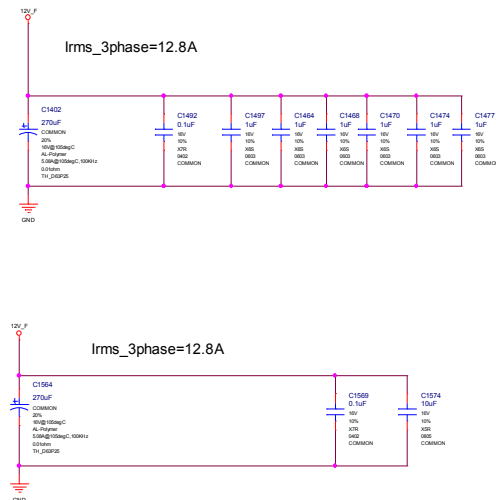
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PCB REV	1G166-A02
BOM REV	A

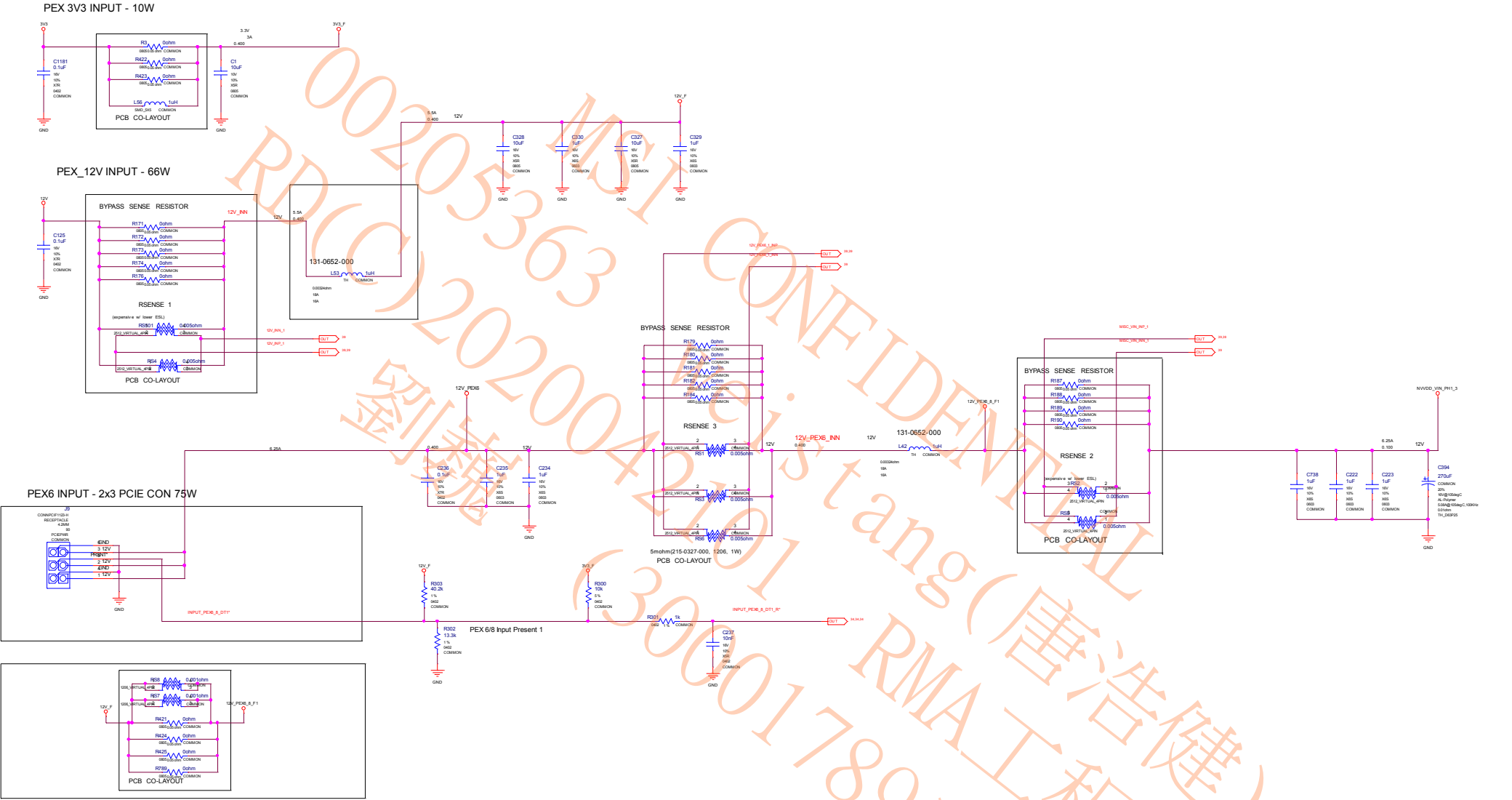
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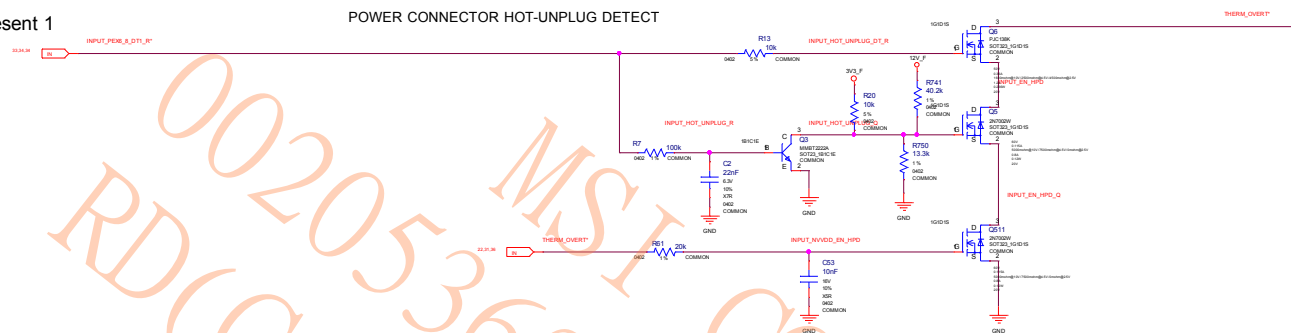
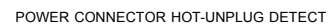
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NV_PN		600-1G166-BASE-A02	
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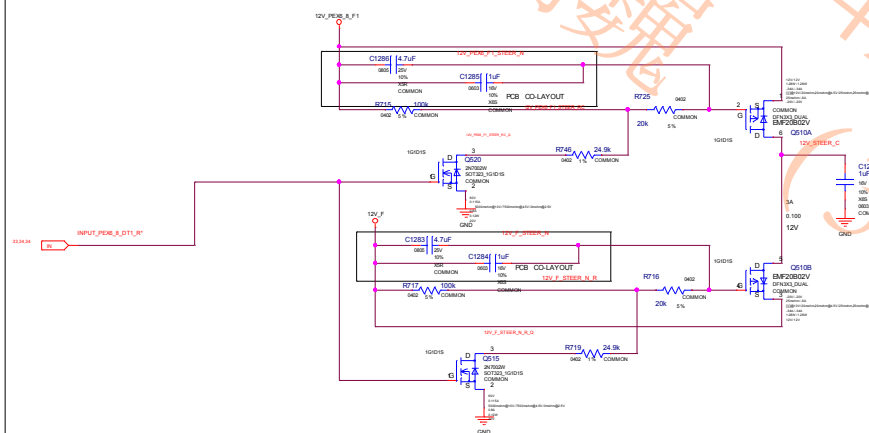
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ASSEMBLY	ASSEMBLY DESCRIPTION
PS: Input, Filtering, and Monitoring	

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GUIDES CURRENT FROM PEX EDGE TO PEX 6 PIN INPUT AREA



GPI012_LOW_PERF*	CPU SPEED
0	Slow
1	Normal

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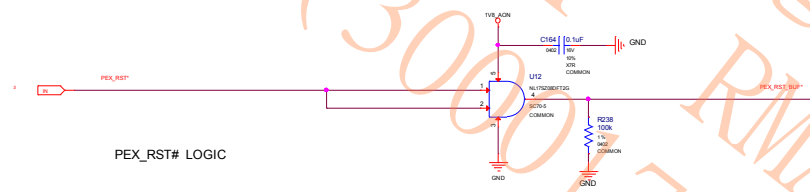
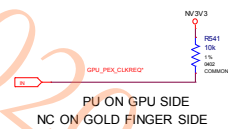
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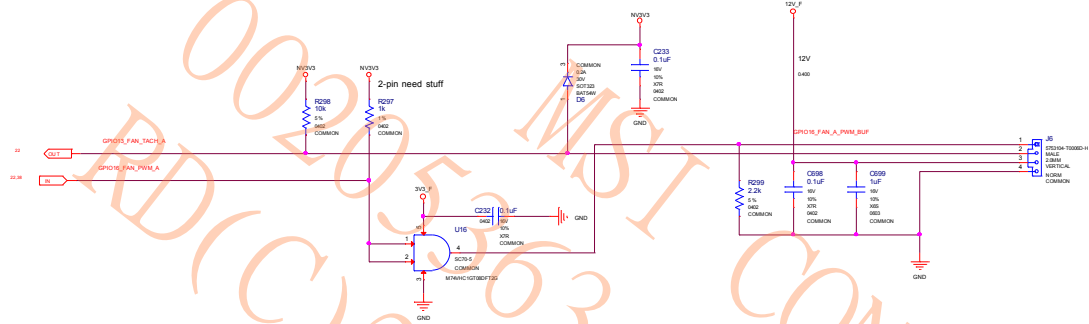


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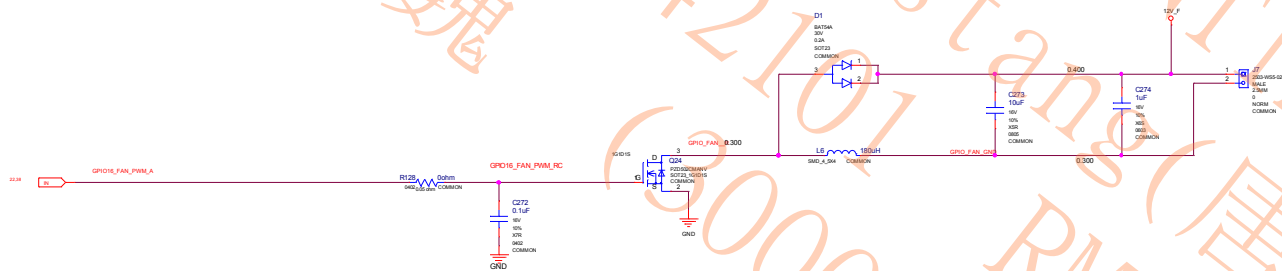
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4-PIN FAN



2-PIN FAN



- FM1 SW_FB
- FM2 SW_FB
- FM3 SW_FB
- FM4 SW_FB
- FM5 SW_FB
- FM6 SW_FB
- FM7 SW_FB
- FM8 SW_FB


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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	FAN

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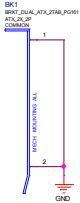
ENABLE PIN STATUS	MULTIPLEXER OPERATION
LOW	FULLY FUNCTIONAL
TRI-STATE(FLOATING)	LIMITED FUNCTION
HIGH	STANDBY

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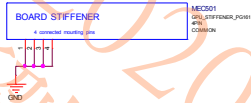


A	B	C	D	E	F	G	H
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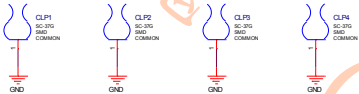
Bracket:



BACK STIFFENER:



EMI CLIP:



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		A		01-AUG-2019	